

## **Government College of Engineering**

Station Road, Osmanpura, Aurangabad – 431 005

Phone: (0240) 2366101,2366110,111 E-Mail – principalgeca@yahoo.com

Fax: (0240) 2332835 Web - http://www.geca.ac.in

No. GECA/EED /2019-20/3 522 To.

Date

5 NOV 2019

GECA Notice Board/ GECA Website

## Subject :- Quotation for Equipment of Electrical Department.

Dear Sir, You are requested to send your competitive quotations for the supply of the following items subject to the following conditions.

## **Terms & Conditions -**

- 1 Rates quoted should be FOR AURANGABAD or free delivery at the Institute inclusive of all lead and Lift.
- 2 Detailed specifications of the articles you intend to supply should be given. If not according to the specification, laid down here under.
- 3. The material should be supplied within (07) days from the date of order. List of material is given below.
- 4. The earliest delivery period should be quoted if you cannot supply within the period mentioned
- 5. Quotation should be in sealed cover and superscripted as "Quotations" for Equipment of Electrical Department. Due on: 14 - 11 -2019, at 5.00 P.M.
- 6. Quotation should be valid for 31.03.2020
- 7. Right to reject any or all quotations are reserved with the under signed.
- 8. Rates quoted must be inclusive of All applicable Taxes.
- 9. Delivery of the material will be carried out free of cost at our institute in EED Dep. 1st Floor in
- 10 Installation of the material will be carried out free of cost at our institute by the supplier.
- 11. Demonstration & Training Must be given to Staff
- 12. No advance shall be paid and No part payment shall be made.
- 13.Quotation not complying with the above conditions and incomplete once will not be considered

5 in 1 Testlab				
The unit shall comprises of several instruments in One Package Dual Trace Oscilloscope,				
Component Tester/Comparator, Triple Output DC Power Source, Frequency Counter and				
Function Generator.				
The following are the required specifications-				
DUAL TRACE OSCILLOSCOPE				
VERTICAL	Deflection	1mV/div to 20V/division. 5mV/div to 20V/div in 12		
DEFLECTION	Coefficient (CH1 &	calibrated steps in 1-2-5 sequence, x5 Magnification		
	CH2)	increases the sensitivity to 1mV/div & 2mV/div		
•		(LED indication)		
	Accuracy	±3%.		
	Bandwidth	DC - 30MHz (-3dB), dc coupled. 10Hz - 30MHz (		
		3dB), ac coupled. 20MHz (-3dB) in x5 MAG		
	Rise Time .	11.6ns or Less, 17.5ns in x5 MAG		
	Display Modes	CH1, CH2, CH1 & CH2 Alternate or Chon mode		
· ·		Algebraic addition CH1+CH2. Algebraic subtraction		
		CHI-CH2, CH2 Inverted and X-Y		
	Input Impedance	$1M\Omega$ and $25pF$ (approx)		
	Maximum Input	400V (dc + peak ac)		
	Voltage			
TIME BASE	Sweep Speed	18 calibrated steps. 0.5μs/div to 0.2s/ div in 1, 2 & 5		

		sequence
	- ifiar	sequence x5Magnification extends the sweep speed to 100ns/div. x5 Magnification indication with LED
	Sweep Magnifier	100ns/div. x5 Magnification indication with
		130/2
	Accuracy	#3% Uncalibrated continuously variable control between
	Variable	steps, extends fastest sweep speed to 40ns/div
	Variable	(approx). (Uncal LED indication)
		(approx). (Uncar LED indication)
	Hold-off Time	4:1 (approx.) variable control
	Triggering Mode	Automatic or Normal with Level control
TRIGGER SYSTEM	Triggering Wood	CH1 / CH2 / LINE / EXT
	Source	Positive or Negative
	Slope	/ L / HE might on TV V / TV-H
	Coupling	Internal: AUTO 1 div 30Hz - 30MHz NORM 1 div
	Trigger Sensitivity	1 211- 20MLIz
		External: AUTO 1Vp-p 30Hz-30MHz NORM 1Vp-p
		3Hz-30MHz(Typical 40MHz at 2 div)
		3HZ-30WHZ(Typical 40WHZ at 2 as 7
HORIZONTAL	Deflection	Same as CH2
DEFLECTION	Coefficient	77 (AMI) (2 ID)
DEFLECTION	Bandwidth .	DC-1MHz (-3dB)
	Input Impedance	1MΩ and 25pF (approx.).
ODNIED II	Cathode Ray Tube	140mm Rectangular Screen, Internal Graticule, 8 x
GENERAL	Cathodo Kay	10 cm, P31 phosphor
	Accelerating	2 kV
	potential	
A THE RESIDENCE AND A SECOND	Trace Rotation	Front Panel Control, allows +5O of trace adjustment
		TTI Lavel
	Z-Modulation	Provides 0.2V ±2%, 1KHz square-wave output for
	Calibrator	probe compensation
	- COMPA	DATOP
DUAL COMPONENT	T TESTER / COMPA	8.6V rms
	Test Voltage	
	Test Current	28mA max
		COLL (MAINIC)
	Test Frequency	50Hz or 60Hz (MAINS)
TRIPLE OUTPUT D	Test Frequency	•
TRIPLE OUTPUT Do	Test Frequency C SOURCE	regulated DC outputs $+5V$ , $\pm 12V$ , $+5V$ at 1A max. and
The unit chall comprise	Test Frequency C SOURCE	regulated DC outputs $+5V$ , $\pm 12V$ , $+5V$ at 1A max. and
TRIPLE OUTPUT DO The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE	regulated DC outputs $+5V$ , $\pm 12V$ , $+5V$ at 1A max. and
The unit chall comprise	Test Frequency C SOURCE es of three independent continuous: (common fl	regulated DC outputs $+5V$ , $\pm 12V$ , $+5V$ at 1A max. and oating wrt ground) $\pm 2\%$ .
The unit chall comprise	Test Frequency C SOURCE es of three independent continuous: (common fl Line / Load Regulation	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s
The unit chall comprise	Test Frequency C SOURCE es of three independent continuous: (common fl Line / Load Regulation Ripple	regulated DC outputs $+5V$ , $\pm 12V$ , $+5V$ at 1A max. and oating wrt ground) $\pm 2\%$ .
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fl Line / Load Regulation Ripple Termination	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel
The unit chall comprise	Test Frequency C SOURCE es of three independent continuous: (common fl Line / Load Regulation Ripple Termination CTION GENERATOR	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fl Line / Load Regulation Ripple Termination CTION GENERATOR Frequency Range	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fl Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOR Frequency Range Output Waveform DC Offset	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fl Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOR Frequency Range Output Waveform DC Offset	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fl Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fl Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy: ±0.5dB per 20dB
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy : ±0.5dB per 20dB step at 1KHz
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy: ±0.5dB per 20dB
The unit shall comprise ±12V at 200mA max. c	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance Attenuator  Sine Distortion	regulated DC outputs $+5V$ , $\pm 12V$ , $\pm 5V$ at 1A max. and oating wrt ground) $\pm 2\%$ .  Less than $8mV$ r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square $\pm 5V$ (across $50\Omega$ ), $\pm 10V$ (open circuit) $10V$ p-p for all functions (with Voltage $50~\Omega$ loads). $20V$ p-p open circuit. (DC $\pm 10V$ AC PK not exceed $\pm 10V$ across $\pm 10V$ across $\pm 10V$ (nominal). $0dB$ to $\pm 10V$ to $\pm 10V$ fine attenuation by amplitude control. Accuracy: $\pm 10V$ A
The unit shall comprise ±12V at 200mA max. o	Test Frequency C SOURCE es of three independent continuous: (common file Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance Attenuator  Sine Distortion REQUENCY COUNT	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy : ±0.5dB per 20dB step at 1KHz  Less than 3% upto 200 KHz
The unit shall comprise ±12V at 200mA max. c	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOR Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance Attenuator  Sine Distortion REQUENCY COUNT Frequency Range	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy : ±0.5dB per 20dB step at 1KHz  Less than 3% upto 200 KHz  TER  20Hz to 100MHz (Usable)
The unit shall comprise ±12V at 200mA max. c	Test Frequency C SOURCE es of three independent continuous: (common file Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance Attenuator  Sine Distortion REQUENCY COUNT	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy : ±0.5dB per 20dB step at 1KHz  Less than 3% upto 200 KHz  ER  20Hz to 100MHz (Usable)  0.01, 0.1, 1 or 10 sec (switch selectable). Accuracy :
The unit shall comprise ±12V at 200mA max. c	Test Frequency C SOURCE es of three independent continuous: (common file Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance Attenuator  Sine Distortion REQUENCY COUNT Frequency Range Gate Time	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy : ±0.5dB per 20dB step at 1KHz  Less than 3% upto 200 KHz  TER  20Hz to 100MHz (Usable)  0.01, 0.1, 1 or 10 sec (switch selectable). Accuracy : ±1 count ± Time Base Accuracy
The unit shall comprise ±12V at 200mA max. c	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOR Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance Attenuator  Sine Distortion REQUENCY COUNT Frequency Range	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy : ±0.5dB per 20dB step at 1KHz  Less than 3% upto 200 KHz  TER  20Hz to 100MHz (Usable)  0.01, 0.1, 1 or 10 sec (switch selectable). Accuracy : ±1 count ± Time Base Accuracy  10MHz Crystal Oscillator. Accuracy : ±20 ppm at
The unit shall comprise ±12V at 200mA max. c	Test Frequency C SOURCE es of three independent continuous: (common fill Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance Attenuator  Sine Distortion REQUENCY COUNT Frequency Range Gate Time Time Base	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (mominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy : ±0.5dB per 20dB step at 1KHz  Less than 3% upto 200 KHz  TER  20Hz to 100MHz (Usable)  0.01, 0.1, 1 or 10 sec (switch selectable). Accuracy : ±1 count ± Time Base Accuracy  10MHz Crystal Oscillator. Accuracy : ±20 ppm at 25OC. Stability : ±30 ppm over 0OC to 50OC range
The unit shall comprise ±12V at 200mA max. c	Test Frequency C SOURCE es of three independent continuous: (common file Line / Load Regulation Ripple Termination CTION GENERATOF Frequency Range Output Waveform DC Offset Maximum Output  Output Impedance Attenuator  Sine Distortion REQUENCY COUNT Frequency Range Gate Time	regulated DC outputs +5V, ±12V, +5V at 1A max. and oating wrt ground)  ±2%.  Less than 8mV r.m.s  2mm Jacks on the Front Panel  0.02Hz to 2MHz in 8 Decade ranges  DC, sine, triangle, square  ±5V (across 50Ω), ±10V (open circuit)  10V p-p for all functions (with Voltage 50 Ω loads).  20Vp-p open circuit. (DC + AC PK not exceed 10V across 50 ohms)  50 Ω (nominal).  0dB to 40dB in steps of 20dB, 20dB fine attenuation by amplitude control. Accuracy : ±0.5dB per 20dB step at 1KHz  Less than 3% upto 200 KHz  TER  20Hz to 100MHz (Usable)  0.01, 0.1, 1 or 10 sec (switch selectable). Accuracy : ±1 count ± Time Base Accuracy  10MHz Crystal Oscillator. Accuracy : ±20 ppm at

	Inputs (Switch Selectable)  Sensitivity  Input Impedance Display	General: Frequency of Function Generator. Scope: Signal through triggered channel of scope. Ext: External signal through BNC connector Scope Input: Min. 4 div p-p swing on oscilloscope. Ext Input: 20mV rms over 10Hz to 50MHz. 50mV rms over 50MHz to 100MHz max. 5V p-p Ext Input: 1MΩ / 30pF 7 segment LED display indicates freq-uency in Hz,
GENERAL		KHz.& MHz
	Power Requirement	230V AC ±10%, 47-65Hz, 90VA. or 115V AC ±
	Dimensions	235 (H) x 315 (W) x 420 (D) mm (approx.).
	Weight	10 Kg. approx
	Accessories	Instruction Manual - 1 No.
		Input Lead BNC to Crocodile - 2 Nos. Component Test Lead (Set) - 1No. 50 ohms Termination - 1 No. BNC to BNC Lead - 1 No.
Make	Aplab /L&T /Systro	

Yours faithfully

Head of Electrical Engineering Govt. College of Engineering Aurangabad