

CURRICULUM VITAE

Vikul Jagansing Pawar
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[Career Objective]

To work in academic, administrative, professional and challenging environment where in I can effectively use my experience and skills for the benefit and growth of the society and organization.

[Personal Information]

- **Full Name** : Vikul Jagansing Pawar
- **Date of Birth** : 11 Mar 1984
- **Marital Status** : Married
- **Languages known** : English, Hindi and Marathi
- **Local Address** : A-1, Warden Quarter, Government College of Engineering, Osmanpura, Aurangabad 431005.

[Qualification]

- **M. E. Computer Science and Engineering**, From Government College of Engineering Aurangabad, 2014, Distinction (**8.625 CGPA**).
- **B. E. Computer Science and Engineering**, From Babasaheb Naik College of Engineering Pusad, 2006, First Class (**68.74%**).
- **HSC**, from Anglo Hindi Jr College Yavatmal, Amravati board.
- **SSC**, from G.D. High school pusad, Amravati board.

[Academic and Professional Experience]

Total Experience: 11 Years 6 Months.

- **8 Years** of experience as an **Assistant Professor** in Computer Science & Engineering Dept., **Government College of Engineering Aurangabad**, (from 07 January 2011 to till date).
- **18 months** of experience as a **Lecturer** in Computer Science & Engineering Dept., in **Babasaheb Naik College of Engineering Pusad**, (from July 2009 to Dec 2010).
- **2 Years** of experience in Mainframe Software Development at **CapGemini India Pvt Ltd. Pune**, Maharashtra, as an Associate Consultant, (from June 2007 to June 2009).

[Teaching and Administrative Experience/Responsibility]

- Working as **Assistant Professor in Computer Science and Engineering** department taught various subject to First year, Third year, and Final Year of Computer Science Engineering Students.
- Worked as incharge of Facilitation Center for DSE Engineering Admission **2017-18**.
- Worked as Officer incharge of FC/ARC for Year 2011, 2012, 2014, 2016, 2017, 2018.
- Working As **Departmental Controller of Examiner for Computer Sc. Engg. And MCA** department from last **3 Years**. Following responsibility is handled while working as DCOE.
 - ❖ Preparation of Panel of Examiner for ESE theory and practical examination
 - ❖ Preparation and conduction of theory examination for two departments.
 - ❖ Preparation and conduction of Practical examination for two departments.
 - ❖ Arranging the Examiner for Practical Exam as per Examiner panel approved by DSB and Examination section.
- Working as **Warden for Boys hostel** from last 8 years in Government College Engineering Aurangabad, while handling this portfolio worked on various activities related to students and at Institute level, mention as follows.
 - ❖ Preparation of hostel allotment as per category reservation and on merit basis for hostel admission.
 - ❖ Advisory board Member for Co-operative MESS in Boys Hostel
 - ❖ Advisory board Member for Co-operative MESS in Girls Hostel
 - ❖ Worked as care taker of all issues related to Mess contractor of food provided by his agency.
 - ❖ Taking care of maintenance, procurement of items for hostel.
 - ❖ Incharge of Health care unit, under this portfolio I have established **MOU with Shraddha Hospital Aurangabad** for getting quick response in case of emergency, and providing free medical facility to all students in campus.
 - ❖ Incharge member of Security, under this portfolio we have indentified well reputable security agency thru Service Charge base tender processing. And looking after improvement in quality of service and duty provided by agency.
 - ❖ Incharge member of cleaning committee, under this portfolio we have indentified well reputable Cleaning agency thru Service Charge base tender processing. And looking after improvement in quality of service provided by agency.

- Worked as coordinator for various students council and students activities, Such as COMPSA (Computer Students Association) Coordinator, Class Teacher, Departmental Training and Placement coordinator, Member of ARC.
- Working as Departmental purchase committee member.
- Worked as Departmental Procurement coordinator for TEQIP Phase II.

[Teaching Interests]

- Data Analytics
- Software Testing and Quality Assurance
- Software Engineering
- Software Project Management
- Software Programming and Compiler Construction
- Distributed Database
- Advance Computer Network (Post Graduate level subject)
- Computer Programming (C, C++, VB)
- Information Security (Post Graduate level subject)
- Database Management System
- Mainframe COBOL Programming.

[Publications]

1. Published a paper titled ***“Enhancement In Brain Tumor Diagnosis Using Mri Image Processing Techniques”*** at ICAICR-2018 Sponsored by ***Springer and CSI*** at Shimla India during 14-16 July 2018.
2. Published a paper titled ***“Android based American Sign Language Recognition System with Skin Segmentation and SVM”*** in **IEEE International conference** at IISC Bangalore India 2018.
3. Published a paper titled ***“Structuring Cloud Computing Using Big Data Analytics Solution: A Survey”*** at **IEEE International conference** on Communication and Electronics Systems (ICCES) 2016 at PPG Institute Coimbatore India.
4. Published a paper titled ***“Feature Extraction and selection from MRI Images for the brain tumor classification”*** at **IEEE International conference** on Communication and Electronics Systems (ICCES) 2016 at PPG Institute Coimbatore India.
5. Published a paper titled ***“Enhancing Information Security in Cloud Computing Environment Using Cryptographic Techniques”*** at **IEEE International conference** on

Communication and Electronics Systems (ICCES) 2016 at PPG Institute Coimbatore India.

6. Published a paper titled "***Examining the Document Image Binarization Technique for Degraded Document Images***" at International conference on Technology and Business Management Dubai 2014.
7. Published a paper titled "***An Improved Binarization Technique For Degraded Document Images Using Local Thresholding Method***" at International Conference On Emerging Trends In Computer Science & Engineering (Icetcse-2014) And International Journal Of Pure And Applied Research In Engineering And Technology
8. A book published of subject "***Elements of Electronics and Computer Engineering***" for First Year Engineering students of Dr. Babasaheb Ambedkar Marathwada University Aurangabad.

[Workshop/Expert Lecture Conducted]

- Conducted the ONE WEEK STTP Under TEQIP-III on "**Python Programming For Image Processing**" at Government College of Engineering Aurangabad during 19th to 23rd September 2018, Worked as **Coordinator** for this STTP.
- Conducted the ONE WEEK STTP Under TEQIP-II on "**Network Security**" at Government College of Engineering Aurangabad during 7th to 11 Oct 2013, Worked as **Coordinator** for this STTP.
- Delivered Expert lecture on "**Software Testing Technique**" at Government Polytechnic Ambad, on 11 September 2013.
- Delivered Expert lecture on "**Software Testing Technique**" at Government Polytechnic Jalna, on 11 September 2013.
- Conducted online exam and interview for the post computer operator in Aurangabad **collector office** in CSE Department of GECA.
- Conducted the Mainframe trainings for the fresher's at **CapGemini Pvt Ltd. Pune**.

[Member of Professional Bodies]

- Life Time member of ISTE (Membership No. LM-78228).
- Life Time member of International Society for Research and Development (ISRDR)
- Life Time Membership of the International Association of Engineers (IAENG).
- Member of World Academy of Science, Engineering and Technology (WASET).
- Member of Institute Research Engineers and Doctors (IREDD)

[Workshop/Conference Attended]

- Attended the ONE WEEK TEQIP-STTP on **“Recent advances in Electromagnetics, RF and Radar Signal Processing”** at Government College of Engineering Aurangabad, , during 19th – 23rd March 2018.
- Attended the ONE WEEK on **“Advance computational Tools in Engineering & Technology”** at Government College of Engineering Nagpur, Sponsored by Directorate of Technical Education Mumbai, during 19th – 23rd March 2018.
- Attended the ONE WEEK AICTE FDP on **“Cloud Computing”** at Government Polytechnic Aurangabad, Organized by NITTTR Chandigarh during 23rd-27th October 2017.
- Attended the ONE WEEK TEQIP-STTP on **“Optimization in Machine Learning”** at Government College of Engineering Aurangabad during 27th Feb to 03 March 2017.
- Attended the ONE WEEK TEQIP-STTP on **“Signal and Image Processing (SIP 2017)”** at Government College of Engineering Aurangabad during 6th to 10 Feb 2017.
- Attended the TWO WEEK AICTE FDP on **“Computation and Optimization with Matlab”** at NITTTR Kolkata during 26th December 2016 to 06 Jan 2017.
- Attended the ONE WEEK TTT Program (Sponsored by DTE Maharashtra and NASSCOM) on **“Foundation Skill in Information Technology”** at Government College of Engineering Aurangabad during 27th June 2016 to 02nd July 2016.
- Attended the ONE WEEK FDP on **“Self-Awareness and Higher Goals in Education”** at IIT Madras during 30th May to 03rd June 2016.
- Attended the ONE WEEK TEQIP-STTP on **“Pedagogy to Synergogy”** at Government College of Engineering Aurangabad during 7th to 11 Dec 2015.
- Attended the TWO WEEK AICTE FDP on **“Embedded System Design Using Altium's IDE and FPGA”** at College of Engineering Pune during 15th to 26 June 2015.
- Attended the International Conference On Emerging Trends In Computer Science & Engineering (Icetcse-2014) at B.N.C. of Engg Pusad during March 2014.
- Attended the FIVE WEEK **“Foundation Training program”** at YASHDA Training Institute Government of Maharashtra Pune during 10th Feb 2014 to 22nd March 2014.
- Attended the ONE WEEK TEQIP-STTP on **“Research Methodology”** at Govt College of Engineering, Aurangabad during 16th to 20th Dec 2013.
- Attended the ONE WEEK TEQIP-STTP on **“Network Security”** at Govt College of Engineering, Aurangabad during 7th to 11th Oct 2013.
- Attended the TWO WEEK AICTE FDP on **“Computer Security”** at Walchand College of Engineering Sangali during 17th to 28th June 2013.

- Attended the ONE WEEK TEQIP-STTP on “**Genetic Algorithm and Neuro Fuzzy Applications in Engineering**” at Govt. College of Engineering Aurangabad during 17th to 21st Dec 2012.
- Attended the TWO WEEK ISTE workshop training at B.N.C. of Engg Pusad on “**Image Processing**” during 9th-18th June 2011
- Attended the ONE WEEK workshop training at IIT Bombay (Powai) on “**Compiler Construction**” during 7th-13th Dec 2009
- Attended corporate training sessions on **COBOL, Vision Plus**, at Capgemini training center Pune.
- Attended US ascent neutralization training at Capgemini training center Pune.

[Industry Experience]

Project at Capgemini India Pvt. Ltd.

Client : HSBC Bank. (Aug 2007 – June 2009)

DESCRIPTION:

The project is HSBC credit card for India & Indonesia.

TEAM FUNCTIONALITY:

- Extracting the customer data from different subsystem.
- Formatting and validating the customer data.
- Populating those data on actual statement.
- Generate the statement and sending to Ondemand for storing purpose and to print center for printing purpose.

RESPONSIBILITY:

- Preparation of technical specification document.
- Coding modules as per client request.
- Writing jobs in JCL and programs in COBOL.
- Planning, Executing and Monitoring UAT batch.
- Resolving Defect and Issues in SIT and UAT.
- Handling weekly Status calls with the client.
- Training freshers on various tasks related to work.

Environment: IBM Mainframe, COBOL, JCL, VSAM.

DECLARATION

I hereby declare that the information furnished above is true and to the best of my knowledge.