

BIO-DATA

Name: Sushama Subodh Agrawal

Designation: Assistant Professor

Experience: 24 Years

Educational Qualifications: ME(EC)

Member of Professional Bodies: ISTE LM



Reviewer of following Conferences:

1. International Conference on Industrial Instrumentation & Control 2015 (ICIC 2015)
2. International Conference on Computing and Network Communications (CoCoNet'15)

Publications:

International Journal Papers:

1. P. S. Kurhe, S. S. Agrawal. "Real Time Tracking & Health Monitoring System of Remote Soldier Using Arm7", *International Journal of Engineering Trends and Technology (IJETT)*, V4(3):311-315 Mar 2013. ISSN: 2231-5381.
2. Rahate Kunal B., Dr. A.S. Bhalchandra, S.S. Agrawal, "VLSI Implementation of Digital Image Watermarking", *International Journal of Engineering Research and Technology*, ISSN:2278-0181, Vol. 2, Issue 6 (June -2013)
3. K.M. Khatri, V.R. Ratnaparkhe, S.S. Agrawal, A.S. Bhalchandra, "Image Processing Approach for Malarial Parasite Identification", *International Journal of Computer Applications Proceedings on National Conference on Growth of Technologies in Electronics, Telecomm and Computers -India's Perception*, GTETC-IP: 5-7, May 2014
4. K.M. Khatri, S.S. Agrawal, "Implementation of Discrete Cosine Transform using VLSI", *International Journal of Engineering Research and Technology*, ISSN: 2278-0181, Vol. 3, Issue 4, April 2014
5. Samadhan Rupnar, S.S. Agrawal, "Watermarking in YCbCr Channel using Xilinx System Generator", *International Journal of Engineering and Management Research*, Vol 5, Issue 3, Page Number: 586-592, June 15 ISSN (Online): 2250-0758
6. Mohammad Moiz, S.S. Agrawal, "Character Recognition System using Xilinx System Generator", *International Journal of Engineering and Management Research*, June 15, ISSN (Online): 2250-0758
7. Priyanka Darkunde, S.S. Agrawal, "FPGA Implementation of CT/MRI Image Fusion using Lifting-based Discrete Wavelet Transform", *International Journal of Engineering and Management Research*, June 15, ISSN (Online): 2250-0758

International Conference:

1. Mohammad Bagwan, S S Agrawal, "FPGA Implementation of DWT/IDWT for Image Compression", *International Conference on Advancements in Engineering and Management, (RITS ICAEM-2012)*, Royal Institute of Technology & Science, Hyderabad, India, 28-29 Feb 2012
2. RahateKunal B., Dr. A.S. Bhalchandra, S.S. Agrawal, "Hardware Implementation of Digital Image Watermarking Using VLSI Platform", *International Conference on VLSI and Signal Processing (ICVSP) 10-12 Jan, 2014*
3. PriyankaDarkunde, S.S. Agrawal, "Memory Efficient Architectures for 2-D lifting based discrete wavelet transform: A Survey", *International Conference on Industrial Instrumentation and Control*, 28-30 May 2015
4. RohiniShete, S.S. Agrawal, "IoT Based Urban Climate Monitoring using Raspberry Pi", *International Conference on Communication and Signal Processing*, 1667-1671, IEEE Xplore. April 6-8, 2016
5. RadhikaKabra, S.S. Agrawal, "Robust Embedding of Image Watermark using LWT and SVD", *International Conference on Communication and Signal Processing*, 1642-1646, IEEE Xplore, April 6-8, 2016
6. ShrutiDande, S.S. Agrawal, S.R. Hirekhan, "Implementation of Colour Image steganography using LSB and Edge Detection Technique: A Labview Approach", *International Conference on Communication and Signal Processing*, 1198-1202, IEEE Xplore, April 6-8, 2016
7. AshviniKapgate, S.S. Agrawal, "Edge Based Data Embedding Steganography algorithm using Zed Board", *IEEE International Conference On Recent Trends In Electronics Information Communication Technology*, India, May 19-20, 2017

National Conference:

1. Komal Khatri, V.R.Ratnaparkhe, S.S.Agrawal, A.S.Bhalchandra, "Image Processing approach for Malarial Parasite identification", *National Conference Growth of Technologies in Electronics, Computers and Telecom: Indian Perception - GTECT-2013 Mumbai, Maharashtra, India , 28 & 29 Sept 2013.*

Short Term Training Programme Organized:

1. STTP of one week on System Design using FPGA (Under TEQIP II)
2. STTP of one week on Signal & Image Processing (Under TEQIP II)

Expert Lectures Presented:

1. "Interrupts", Deogiri Engineering College, Aurangabad
2. "Arm Processor", NIELIT Aurangabad
3. Subject External Expert Member of Selection Committee to select Senior Project Faculty in Embedded/VLSI at NIELIT, Aurangabad

Short Term Training Programmes Participated:

1. Soft Computing Tools & its Applications in Engineering (20.06.2011 to 01.07.2011), Government College of Engineering, Aurangabad
2. Skill Matrix Enhancement and effective Teaching-learning for quality Technical education, (28.11.2011 to 09.12.2011), Government College of Engineering, Aurangabad
3. Engineering Optimization Techniques and its application in Research, (02.01.2012 to 12.01.2012), Government College of Engineering, Aurangabad
4. Engineering Optimization and its application, (25.06.12 to 29.06.12), Government College of Engineering, Aurangabad
5. Self-Management and Team Management for Success Breaking the Barriers (18.09.2012), Government College of Engineering, Aurangabad
6. Genetic Algorithm and Neuro Fuzzy Applications in Engineering, (17.12.2012 to 21.12.2012), Government College of Engineering, Aurangabad
7. Ashling Based Real Time Embedded Development System on ARM Processor, (16.01.2013 & 17.01.2013), Government Polytechnic, Nashik
8. System Design using FPGA, (24.06.2013 to 28.06.2013), Government College of Engineering, Aurangabad
9. MSP 430 and its application and ASD with ASLKv2010 Starter kit, (20.09.13 to 24.09.13), Government College of Engineering, Aurangabad
10. Network Security, (07.10.2013 to 11.10.2013), Government College of Engineering, Aurangabad
11. Work Life Balance, (05.12.2014), Government College of Engineering, Aurangabad
12. Outcome Based Accreditation, (03.02.2014& 22 to 24.02.2014), Government College of Engineering, Aurangabad
13. Embedded System DesignFlow Using Vivado, (10/12/2014& 11/12/14), VJTI Mumbai
14. Outcome Based Education &NBA, (31.01.2015 & 01.02.2015), Government College of Engineering, Aurangabad
15. Pedagogy to Synergogy, (07.12.2015 to 11.12.2015), Government College of Engineering, Aurangabad
16. Advances in Digital Signal Processing, (03 and 04 Dec, 2015), Government College of Engineering, Aurangabad
17. Wavelet Theory, (12.04.2016 and 13.04.2016), Government College of Engineering, Aurangabad
18. FPGA based Digital System Design with Application: Learning by doing, (20.06.16 to 25.06.16), Deogiri Institute of Engineering and Management Studies, Aurangabad
19. CMOS MIXED SIGNAL AND RADIO FREQUENCY VLSI DESIGN, (30.01.2017 to 04.02.2017), IIT Kharagpur, Remote Center MarathwadaInstitute of Technology,Aurangabad
20. Signal & Image Processing 2017, (06.02.2017 to 10.02.2017), Government College of Engineering, Aurangabad
21. 3D Printing: A Disruptive Technology of this era, (20.02.2017 to 22.02.2017), Government College of Engineering, Aurangabad
22. Optimization in Machine Learning, (27.02.2017 to 03.03.2017), Government College of Engineering, Aurangabad

Any other information:Recipient for Best Maharashtra Polytechnic Teacher, for 2010-11,
from Engineering Education Foundation, Pune