

BIO-DATA

Name : Mrs. Supriya Prashant Diwan

Designation: Asst. Professor

Experience: 6 years

Educational Qualifications: M.Tech. in Control System

Ph.D. topic: Fast Nonlinear Model Predictive Controller Development

Research Areas: Advanced Control Systems

Publications:

➤ **International Journal : 01**

➤ **International Conference: 01**

1. Supriya P Diwan and Dr. D. S. More, "Precised pitch synchronization of toroidal winding machine using stepper motor drive control", National Conference on Advances in Electrical Engineering at SIT, Ichalkarangi, Kolhapur 8-9 Sept., 2012, Page no. 1-7
2. Supriya P Diwan and Piyush Tekale, "Modified Kalman Based NLMS Algorithm for Noise Cancellation", *International Journal of Science and Technology*, 2012, Vol 5, Issue1, page no. 47-55.

Consultancy/ Testing: Child's health monitoring system tested.

Conferences / Workshop Organized: Workshop on "RF based design".



Short Term Training Programmes/Workshops Participated:

Sr. no.	Name of Workshop/Training	Sponsored authority and Place	Duration
1	Advanced Multivariable Control	CEP IIT Bombay, WCE, Sanli	March 19&24-26, 2017
2	Embedded System Design using Altium's Integrated Development Environment and FPGA	AICTE, COE, Pune	June 15-26, 2015
3	Analog VLSI Design using Cadence Tool	TEQIP-II, COE, Pune	December 10-14, 2014
4	System Design using FPGA(SDF-2013)	TEQIP-II, GCE, Aurangabad	June 24-28, 2013
5	Embedded System Development Using DSP's	AICTE, COE, Pune	May 20-31, 2013
6	Analog System Design & MSP430 Development Tools	TEQIP-II, COE, Pune	November 26-30, 2012

7	FPGA Prototyping using Verilog	AICTE, IIT, Khargpur	June 25-30, 2012
8	Advanced DSP Design Techniques	CEP, IIT, Khargpur	July 02-06, 2012
9	Advanced Control Systems	AICTE, WCE, Sangli	December 5-16, 2011
10	Tarinig Program on OrCad Software	WINSpect Info-system Pvt. Ltd. ,Navi Mumbai and Government College of Engineering Aurangabad	15/01/2013 to 19/01/2013

Any other information: Currently Pursuing PhD at Walchand college of Engineering, Sangli (Electronics Engineering Department) through QIP.