

Recruitment for the Post of Junior Research Fellow (JRF)

Applications are invited for the post of Junior Research Fellow (JRF) in the Centre Of Excellence - Thinking Systems for Signal and Image Processing. The duly completed application on prescribed format along with copies of supporting documents shall be submitted online on the following email ID, at the Department of Electronics and Telecommunication Engg. Govt. College of Engineering Aurangabad, Chh. Sambhajinagar, on or before 20/10/2025. The soft copy of the application should be sent to headetc@geca.ac.in and TSSIP_COExcellence@geca.ac.in

No. of Position: Two (02)

Salary Range: Rs. 30000/- per month

Duration of Appointment: Two (02) years

Age Limit: Maximum 35 Years as on 20/10/2025

Essential Qualifications: The candidate should have M.Tech. degree in Electronics/Computer Science/Electrical Engineering/Instrumentation Engineering/Biomedical Engineering with 60% of marks (CGPA of 6.5 in 10 point scale).

Desirable Qualifications: Candidate must qualify GATE/NET or similar other national level ranking.

Note(s)

1. The selected candidate shall be working with high-end devices such as Multispectral, Hyperspectral sensors, Thermal camera, LiDAR, RGB camera and Biomedical sensors and shall be responsible for data authenticity, data analysis, algorithmic processing.
2. The research work may be based on any particular topic, as directed by the Supervisor leading the publication of research papers in SCI journals, patents, IPRs, etc.
3. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for interview. The Candidates will be shortlisted on the basis of merit and need of the project.
4. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/certificates in-support of their academic qualifications.
5. Applicants in employment (private, government or any other organization) are required to submit a "No Objection Certificate" from the employer at the time of interview.
6. No TA/DA will be paid for appearing in the interview.
7. The first and foremost responsibility of the candidate is to fulfil the jobs' requirements to complete the delegated project work in timely manner.
8. Candidate selected for this position can also apply for the regular PhD program in the Electronics and Telecommunication Engg Department, provided they satisfy the eligibility criterion laid down by the institute/Dr.Babasaheb Ambedkar Marathwada University, Chh. Sambhajinagar.
9. This appointment will be purely contractual. Initially appointment will be for first two year. It will be renewed for another year or till the end of project whichever is earlier based on the performance of the candidate and recommendation of the selection/review committee.

Principal
Government College of Engineering Aurangabad



**Centre Of Excellence
In
Thinking Systems for Signal and Image Processing
Junior Research Fellow**

1. Candidate's Profile:

First Name	
Last Name	
Father's Name	
Mother's Name	
Date of Birth	
Gender: M / F	
Nationality	
PAN No.	
Marital Status	
Name of Spouse	
Whether any Court Cases Pending? (if yes mention details)	
Nearest Police Station from the residence	

2. Contacts:

	Permanent	Correspondence
House No.		
Street Name		
Village / Town / City		
State		
PIN		
Mobile No.		
E-Mail		

3. Academic Qualifications:

S. N.	Degree	Board / University	Year of Passing	Grade Div.	%	Subjects Studied	Xerox Encl. No.

4. Work Experience:

S. N.	Company	Designation	Job Responsibilities	Duration	Xerox Encl. No.

5. Reference:

S. N.	Name	Address	Contact Details

6. Declaration: I hereby, undertake that all the details furnished above are true to the best of my knowledge and I am liable for all disciplinary action, if found otherwise. My candidature may kindly be considered for the above temporary/ position as per the rules of Centre Of Excellence (TSSIP), Govt. College of Engineering Aurangabad, Chh.Sambhajinagar.

Place:

Signature:

Date:

Name:

