BIO-DATA

Name : Mrs. Supriya Prashant Diwan

Designation: Asst. Professor

Experience: 9 years

Educational Qualifications: M.Tech. in Control System

Ph.D. topic:Nonlinear Model Predictive Controller Development for Fast Dynamic Real-Time Systems

Recognized Ph.D. guide at: Nil

Research Areas: Advanced Control Systems, Digital System Design, Embedded System

Research Project Completed: Nil

PATENTS:

➢ **Patents (Design) Obtained: Nil**

➢ **Patents filed and published in patent journal: Nil**

➢ **Copyright filed: Nil**

➢ **Trade Mark registration filed: Nil**

Book Chapter: Nil

Member of Professional Bodies

Reviewer of Journals: Nil

CountriesVisited: Nil

Publications:

➢ **International Journal Papers: 01**

1. Supriya P Diwan and PiyushTekale, "Modified Kalman Based NLMS Algorithm for Noise Cancellation", *International Journal of Science and Technology,* 2012, Vol 5, Issue1,page no. 47-55.

➢ **National Journal Papers: Nil**

➢ **International Conference:01**

1. Supriya P Diwan and Dr. S. S. Deshpande, "Nonlinear Model Predictive Controller for the Real-Time control of Fast Dynamic System", *4th International Conference on Communication and Electronics System(ICCES-2019)*, 2019,ISBN 978-1-7281-1261-9, page no. 290-295.

- ➢ **National Conference: 01**
  1. Supriya P Diwan and Dr. D. S. More,"Précised pitch synchronization of toroidal winding machine using stepper motor drive control", *National Conference on Advances in Elecrtical Engineering* at SIT, Ichalkarangi, Kolhapur 8-9 Sept., 2012, Page no. 1-7

Consultancy/ Testing: Child's health monitoring system tested.

Conferences / Workshop Organized: Workshop on "RF based design".

Short Term Training Programme Organized: Nil

Edited Books Published: Nil

Expert Lectures Presented: Nil

Short Term Training Programmes/Workshops Participated:

Sr. no.	Name of Workshop/Training	Sponsored authority and Place	Duration
	Optimization and Optimal Control:Data based Approach	TEQIP-III , COE, Pune	December 03-07, 2018
1	Recent Innovations in Predictive Control	IEEE, IIT, Bombay	November 25-29, 2018
2	Multiobjective Optimization Using Metaheuristics	GIAN, IIT, Kanpur	March 03-07, 2018
3	Advanced Multivariable Control	CEP IIT Bombay, WCE,Sanli	March 19&24-26,2017
4	Embedded System Design using Altium's Integrated Development Environment and FPGA	AICTE, COE, Pune	June15-26, 2015
5	Analog VLSI Design using Cadence Tool	TEQIP-II, COE, Pune	December 10-14, 2014
6	System Design using FPGA(SDF-2013)	TEQIP-II, GCE, Aurangabad	June24-28, 2013
7	Embedded System Development Using DSP's	AICTE, COE, Pune	May 20-31, 2013
8	Analog System Design & MSP430 Development Tools	TEQIP-II, COE, Pune	November 26-30, 2012
9	FPGA Prototyping using Verilog	AICTE, IIT, Khargpur	June 25-30, 2012
10	Advanced DSP Design Techniques	CEP, IIT, Khargpur	July 02-06, 2012
11	Advanced Control Systems	AICTE, WCE, Sangli	December 5-16, 2011
12	Tarinig Program on OrCad Software	WINSpect Info-system Pvt. Ltd. ,Navi Mumbai and Government College of Engineering Aurangabad	15/01/2013 to 19/01/2013

Conference / Seminar: Nil

Any other information: Currently Pursuing PhD at Walchand college of Engineering,Sangli (Electronics Engineering Department) through QIP.